

ZY8DN02 Triplex Redundant Flight Control Computer

- Triplex redundancy CPU structure
- Bus function module realized by external interface integrated board
- Discrete function module realized by discrete emulator
- Analog function module realized by analog simulation card
- Power module realized by power board
- External power supply of the chassis is powered by AC 220V
- Centralized architecture design
- ARINC659 serial back board bus as the core among channels
- 64-channel discrete input, 64-channel discrete output
- 16-channel analog input, 16-channel analog output
- Input DC 28V.



Product Description

The device adopts a centralized architecture with a triplex similar redundancy structure, and its interchannel takes ARINC659 serial backplane bus as the core. Every function board hooked up to the bus in the form of resources, the device provides basic hardware platform for the software development, testing, comprehensive verification and digital closed-loop of the development course of the flight control system.

Performance Parameters

Redundancy design..... triplex CPU redundant structure
Bus type.....ARINC659 bus interface
Serial port.....provide 2-way of RS422 serial port interfaces per redundancy
Watchdog every redundancy of flight control computer owns watchdog function
Discrete.....64-channel discrete input and 64-channel discrete output
Analog.....16-channel analog input and 16-channel analog output
Power supply.....DC 28V input

Working Environment

Temperature range.....-10℃~65℃
Relative humidity.....5%~95%, no condensation
Shock vibration5g

General Specifications

External dimensions (L×W×H) (without ear)..... 600×320×350 mm
Chassis material.....aviation aluminum alloy

Typical Applications

Construction of automatic test system
Laboratory automation, research and design verification